

Abstracts

A new circuit topology for continuous group delay synthesis

J.D. Fredrick, Yuanxun Wang and T. Itoh. "A new circuit topology for continuous group delay synthesis." 2002 Microwave and Wireless Components Letters 12.3 (Mar. 2002 [MWCL]): 85-87.

A new circuit topology is realized in order to synthesize group delay continuously. The circuit is based upon a new variable coupling coefficient coupled line section and a microstrip feedback loop. The new variable coupler is designed by periodically loading an edge coupled microstrip coupler. With this new design, coupling variation of more than 10 dB may be achieved. To the best of the authors knowledge, this is the first report of this type of microstrip coupler. The complete circuit consumes no dc power and exhibits more than 500 ps of continuous variable true time delay. Insertion loss is better than 3 dB and return loss is better than 15 dB.

 [Return to main document.](#)